

No	Terminal Name	I/O	Description	P.OFF	P.Failure	Reset/ Release
52	CVOUT	O	Output terminal for the composite video signal.	Out	Out	Out
53	5V(OSD)	-	Power supply terminal for OSD	-	-	-
54	HLF	I	LPF connection terminal for slicer.	In	In	In
55	VHOLD	I	Capacitor connection terminal of the Reference voltage generator circuit for the slicer.	In	In	In
56	CVIN(EDS)	I	Composite video signal input terminal for the slicer.	In	In	In
57	GND	-	Connect to the GND (Test terminal "A" in the factory).	-	-	-
58	A.SEARCH (L)	I/O	AGC gain selection signal for tuner CH selection. During digital AFC is working at TUNER PRESET mode: Low (AGC is high speed). Other than above: Hi-Z (AGC is normal speed).	Low	Low	In
59	NC (For NV-FJ730A/EA)	O	Low fix.	Low	Low	Low
	SECAM MIX (For other than NV-FJ730A/EA)	I	SECAM CHROMA signal input terminal for SECAM SIGNAL SUPER IMPOSE.	Low	Low	Low
61	PAL 9H	O	PAL 9H: High is output.	Low	Low	Low
62	SW2	O	(For NV-FJ730A/EA) High fix. (For other than NV-FJ730A/EA) AUDIO SYSTEM = BG, DK: High is output. AUDIO SYSTEM = I, M: Low is output.	Low	Low	Low
63	VBI2	O	NAVI writing terminal. * During NAVI data being written (During recording when JET NAVIGATOR is ON). OSD letters (Including the masking) being written : High Except OSD letters being written : Low * Other than above: Hi-z	Low / Hi-Z	Low / Hi-Z	Low / Hi-Z
64	VBI1/CHARA	O	Terminal for both NAVI Writing and OSD REC Writing. 1. During NAVI writing (During recording in JET NAVIGATOR is ON) At NAVI data "H" being written : High is output At NAVI data "L" being written : Low is output At except NAVI data being written : Low is output 2. During OSD REC (During recording in OSD REC is ON) During OSD letters (Except the masking) being written : High is output Except OSD letters being written : Low * Other than above condition : Hi-z	Low / Hi-Z	Low / Hi-Z	Low / Hi-Z
65	FLD_CS	O	Chip select signal for FIP driver. *Active: "Low" *Non-active: "High"	(Normal ope.)	Low	Low
66	UNLOADING(H)	O	Control terminal for the Unloading operation.	Low	Low	Low
67	LOADING(H)	O	Control terminal for the loading operation.	Low	Low	Low
68	IC.DATA.OUT	O	Timer-Bus signal for peripheral ICs control: Data output	(Normal ope.)	In	Hi-Z
69	IC.DATA.IN	I	Timer-Bus signal for peripheral ICs control: Data input	(Normal ope.)	In	In
70	IC.DATA.CLK	O	Timer-Bus signal for peripheral ICs control:Clock outout	(Normal ope.)	In	Hi-Z
71	IIC.CLK	O	Serial communication terminal (IIC) for IC3001/FM audio IC.	(Normal ope.)	In	Hi-Z
72	IIC.DATA	I/O	Serial communication terminal (IIC) for IC3001/FM audio IC.	(Normal ope.)	In	Hi-Z
73	125Hz/ROM.CORE	O	ROM Correction confirmation mode: * ROM correction setting bit is "ON": "High". * ROM correction setting bit is "OFF": "Low" is existed. (Other than ROM Correction confirmation mode, this terminal is the output terminal of internal clock for main clock adjustment.:Outputting the 125Hz.)	(Normal ope.)	Low	Low
74	CAP R/F	O	The rotation direction control terminal of the capstan driver. *RVS="High" *FWD="Low".	Low	Low	Low
75	TAPE REFRESH (H)	O	Terminal for the signal that changes FE oscillation frequency (130KHz/70KHz) for TAPE REFRESH.	Low	Low	Low
76	CAP.ET	O	Power supply terminal for the capstan motor control. (Compared with the driver reference voltage, when it is "low", current will be cut. Also when it is "high", the rotation speed will be accelerated.)	High PWM=0V	Low	High PWM=0V
77	CYL.ET	O	Power supply terminal of the cylinder motor control: (Compared with the driver reference voltage, when it is "high", current will be cut. Also when it is "low", the rotation speed will be accelerated. (Max:2.8V))	Low PWM=2.800 V	Low	Low PWM=2.800 V